

Notice of References Cited

Application/Control No.

10/528,332

Applicant(s)/Patent Under
Reexamination
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Examiner

JUAN A. TORRES

Art Unit

2611

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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Lattice, "In-System Programming Design Guidelines for ispJTAG Devices", February 2002
	V	Maxim, "DS2409 MicroLAN Coupler", February 7, 2003
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.